**Lab Assignment 9**

1. **Write down the Verilog code for 4-bit ripple counter using JK flip flop and validate the design using university waveform.**
2. **Write down the Verilog code for 4-bit synchronous down counter using JK flip flop and validate the design using university waveform.**
3. **Design and verify 3-bit synchronous up-counter using D flip flop.**